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PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor	Fong PONG	**Examiner:**	Jasmine Song
Serial No.:	09/444,173	**Confirmation No.:**	8306
Filed:	Nov. 19, 1999	**Group Art Unit:**	2187

Title: Asynchronous Cache Coherence Architecture In A Shared Memory Multiprocessor With Point-to-Point Links

Commissioner for Patents
Washington, D.C. 20231

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Technology Center 2100

AMENDMENT/REPLY UNDER 37 C.F.R. §1.116

Sir:

In reply to the Final Office Action mailed September 11, 2002 (the "Office Action"), please amend the application as follows, and consider the following remarks.

IN THE CLAIMS

Please amend the following claims.

1. (Once Amended) A method for accessing memory in a multiprocessor system, comprising:

from a requesting processor, issuing a request for data to one or more other processors and memory, each copy of the data being associated with a state information indicating whether the copy is valid or not;

in each of the processors and memory that receive the request, checking to determine whether a valid copy of the data exists; and